Electronic Acknowledgement Receipt FES ID: 1142796 Application Number: 09976004 Confirmation Number: 9588 Reducing overhead when setting up multiple virtual circuits using Title of Invention: signaling protocols First Named Inventor: Amit S. Phadnis Customer Number: 26392 Filer: Narendra R. Thappeta Filer Authorized By: Attorney Docket Number: CSCO-010/4390 Receipt Date: 07-AUG-2006 Filing Date: 15-OCT-2001 Time Stamp: 09:25:36 Application Type: Utility International Application Number:

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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	resp-5_csco-10.pdf	170272	no	20

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt. in due course.